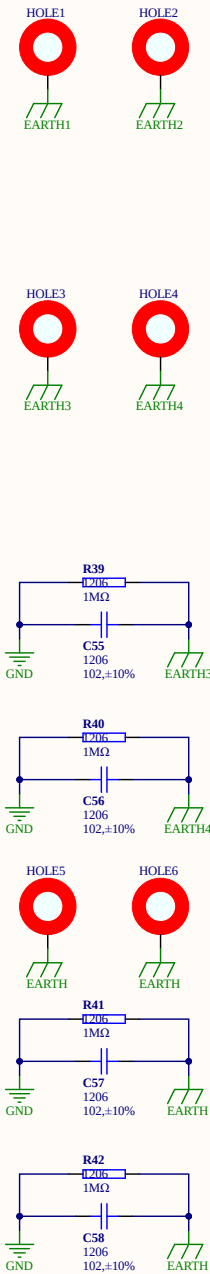
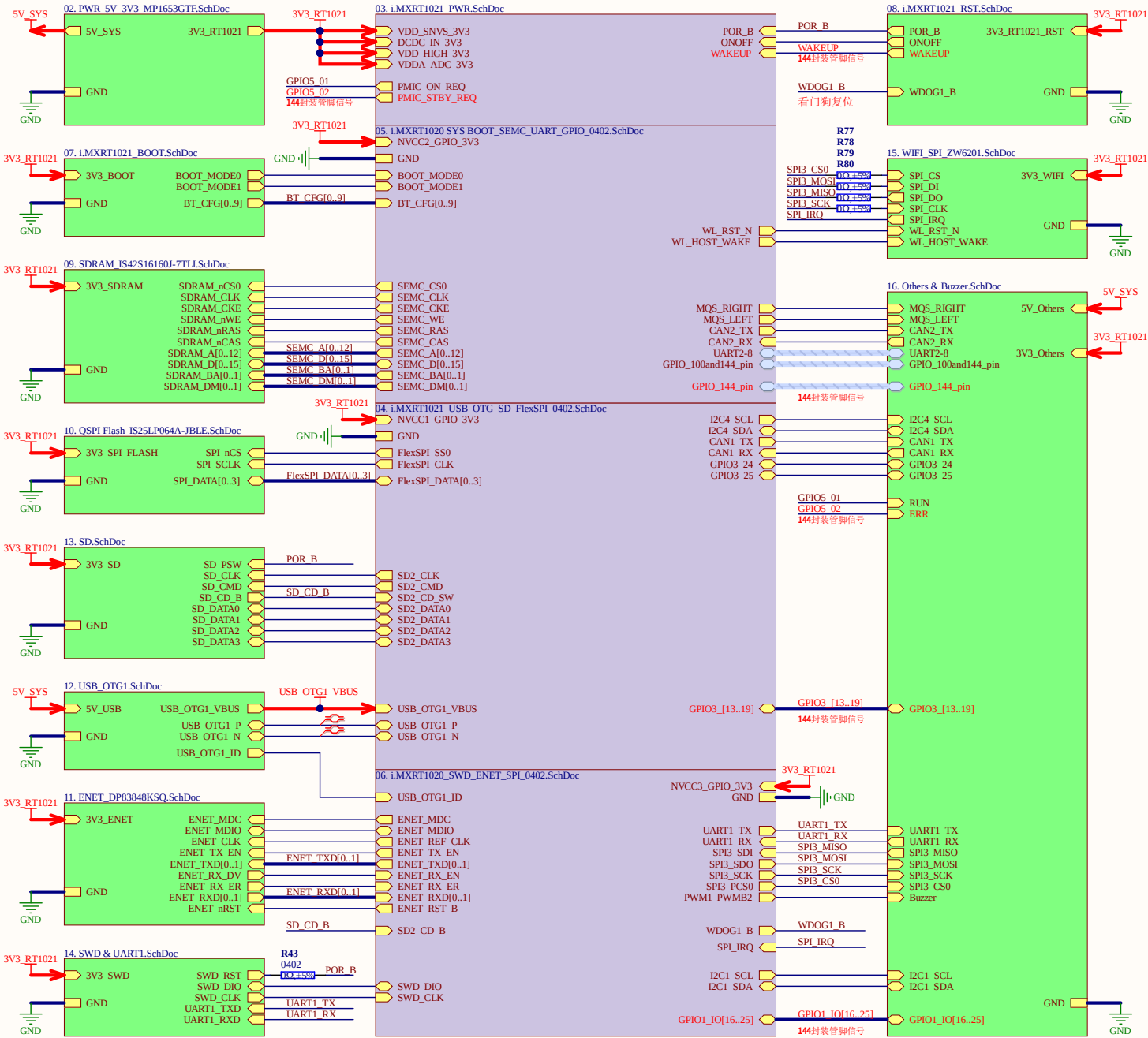


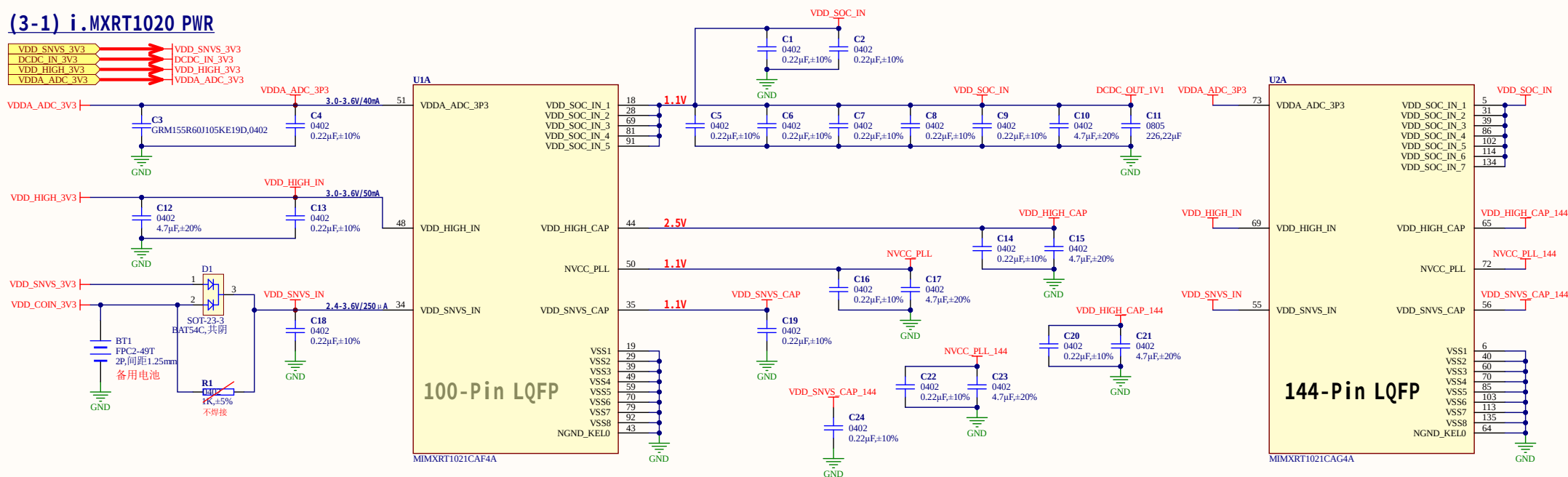
1.MAIN_RT1020



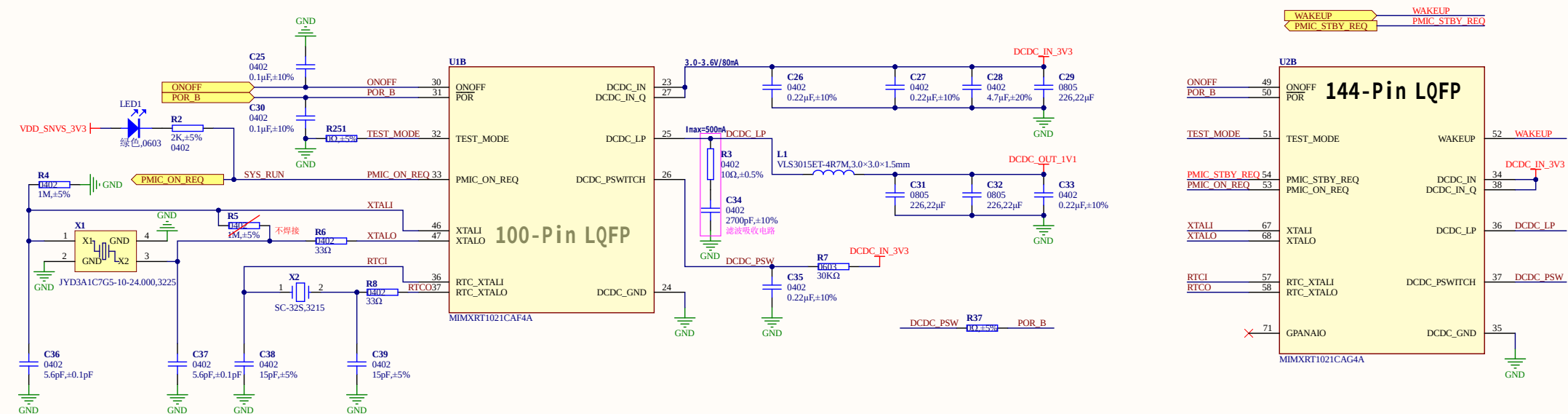
#	修改日期	修改内容
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3.i.MXRT1020 PWR & CLK

(3-1) i.MXRT1020 PWR



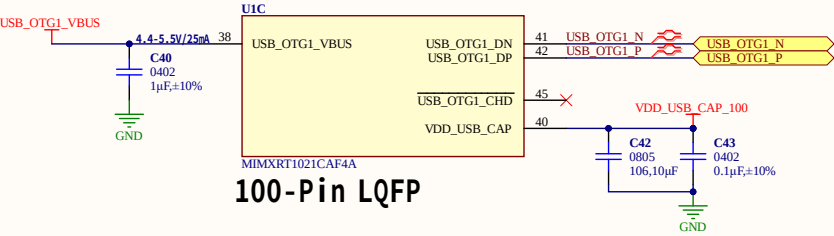
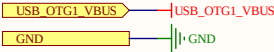
(3-2) i.MXRT1020 DCDC & CLK



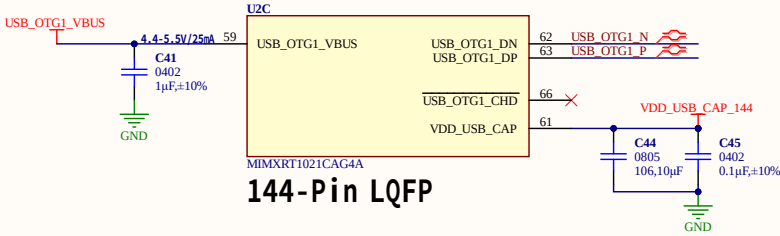
#	修改日期	修改内容
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4.USB_OTG & SD & FlexSPI

(4-1) USB_OTG1

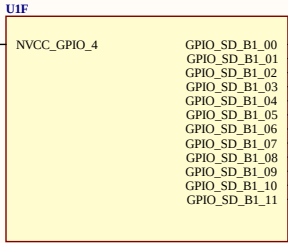
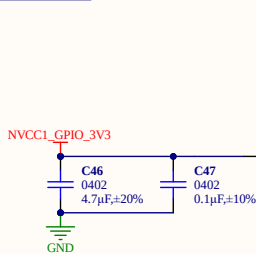


100-Pin LQFP

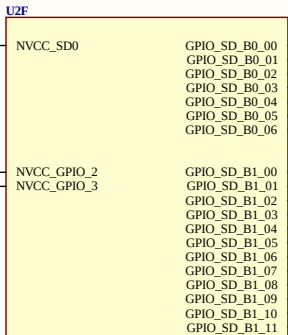
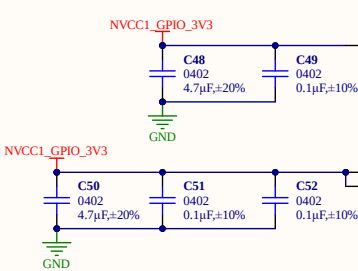
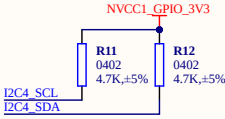
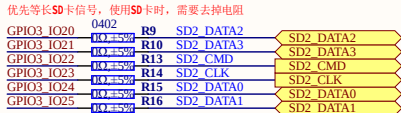


144-Pin LQFP

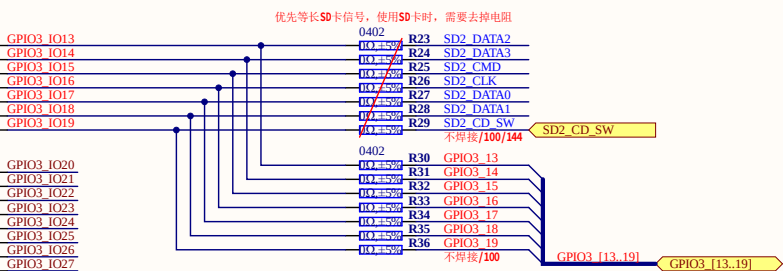
(4-2) SD & CAN1 & I2C4 & SPI FLASH



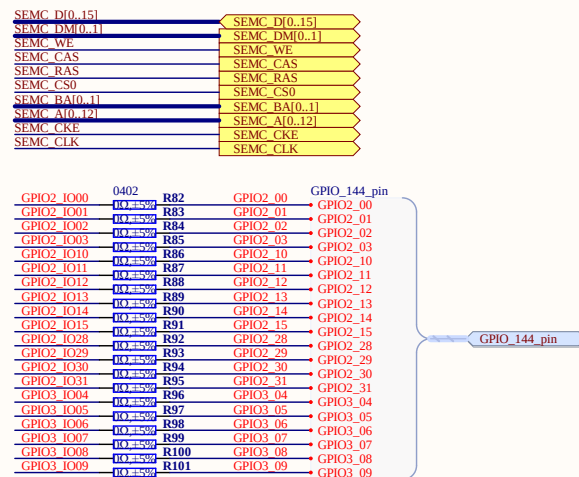
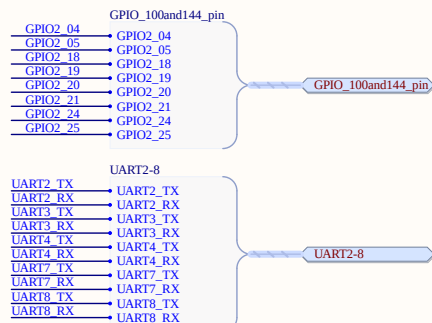
100-Pin LQFP



144-Pin LQFP

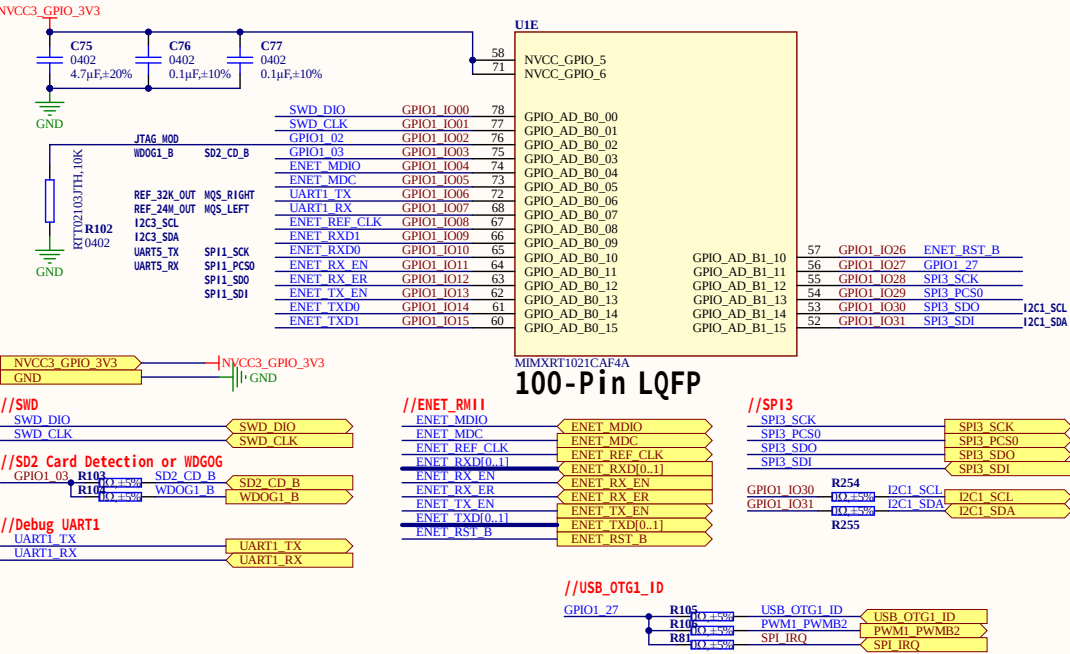


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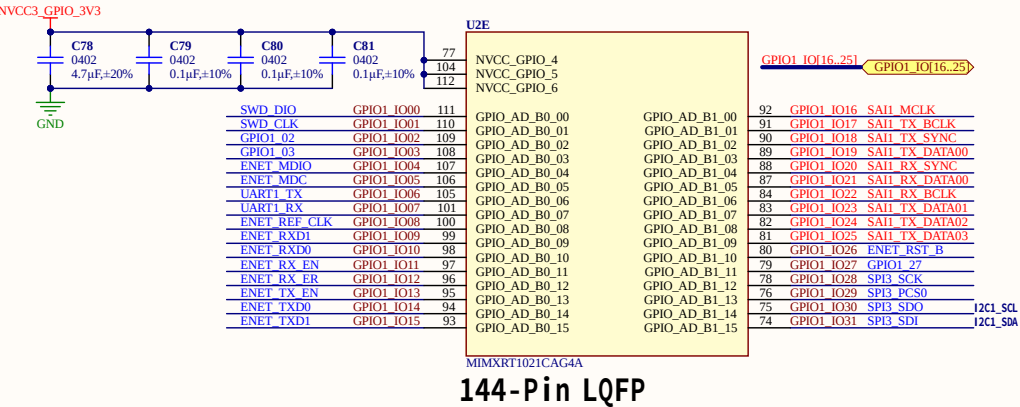


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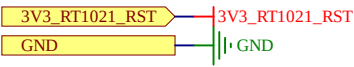
(6-1) SWD & GPIO & UART1 & ENET & USB OTG ID & SPI3



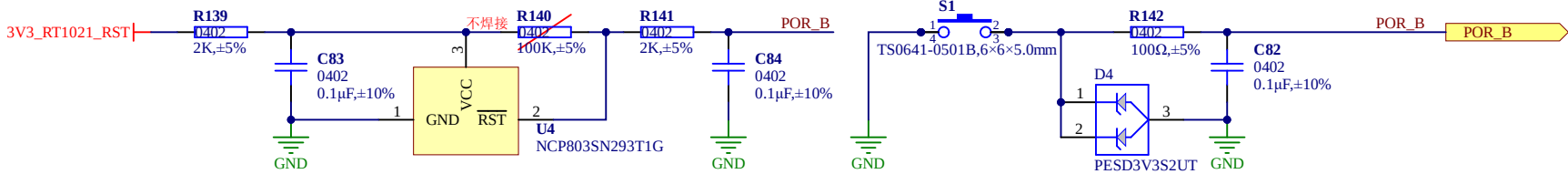
(6-2) SWD & GPIO & UART1 & ENET & USB OTG ID & SPI3 & SAI1



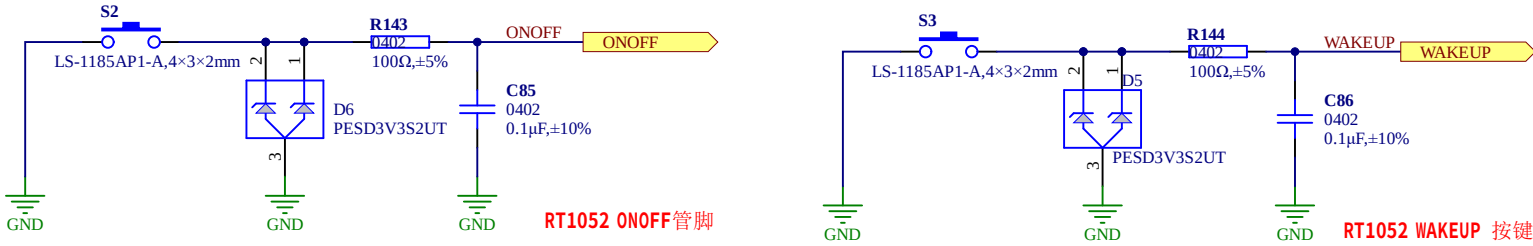
8.i.MXRT1020 RST & ONOFF



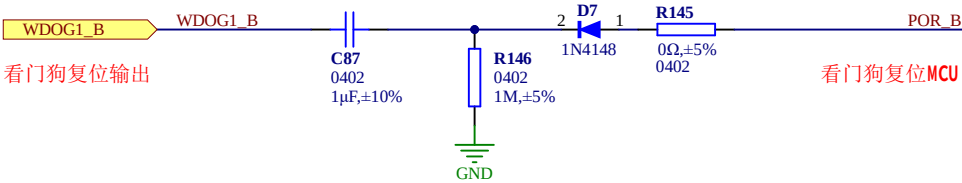
(8-1) RT1020 RST



(8-2) RT1020 FUN_Button



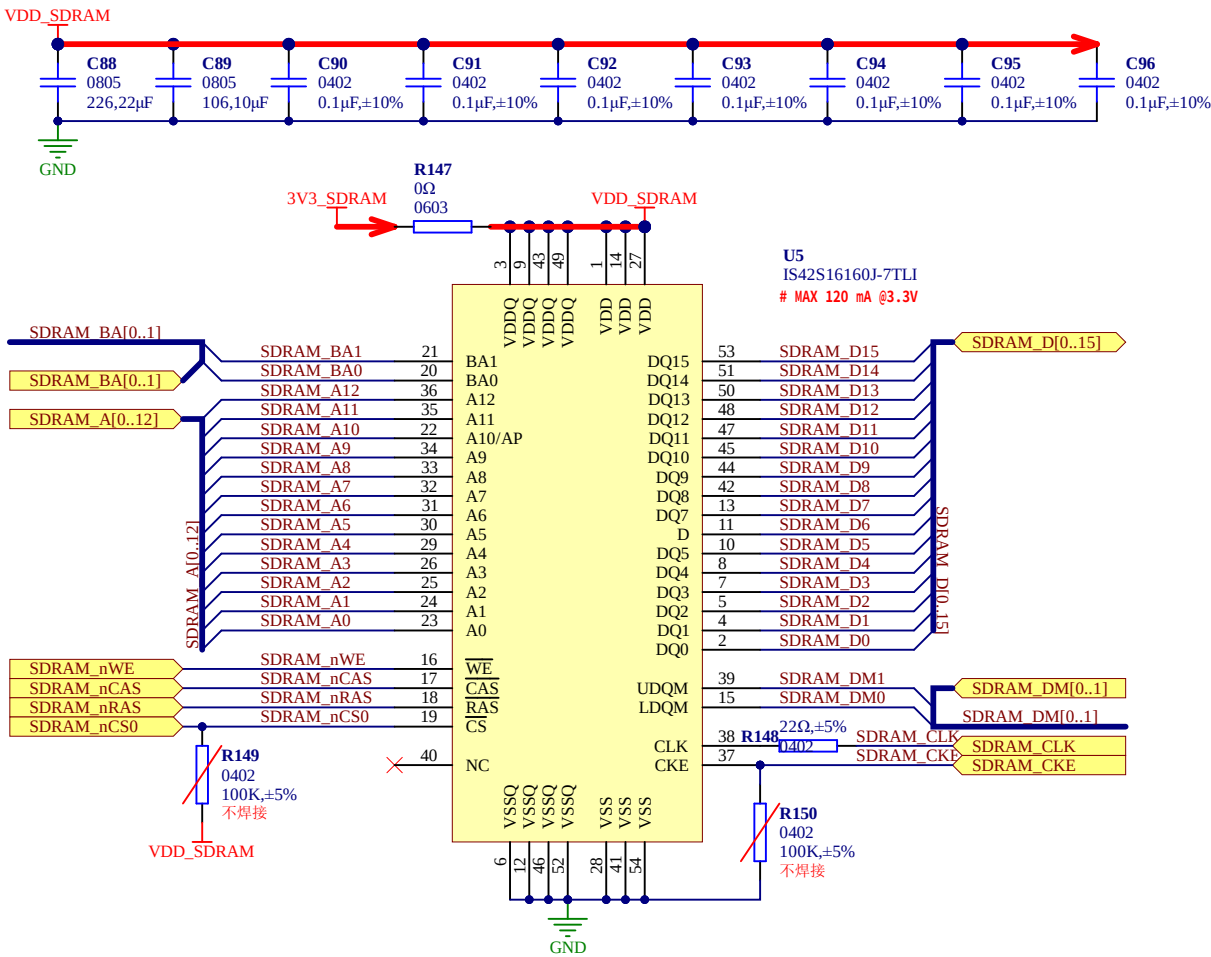
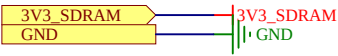
(8-3) SYS_WDG_RST



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9.SDRAM_IS42S16160J-7TLI

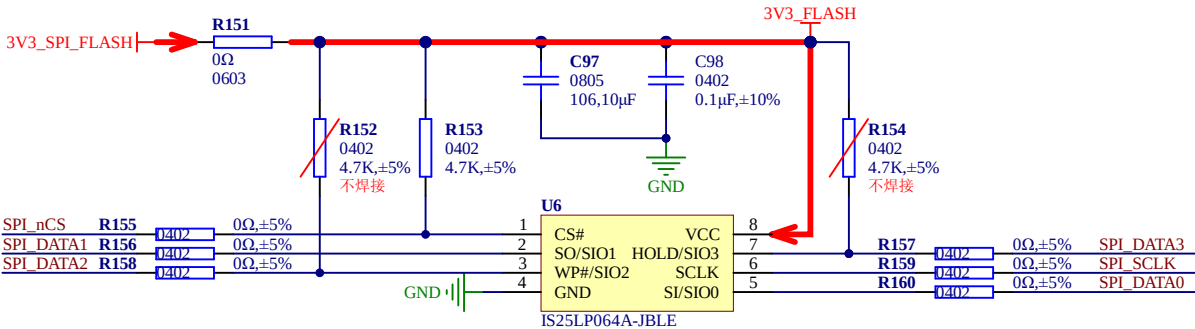
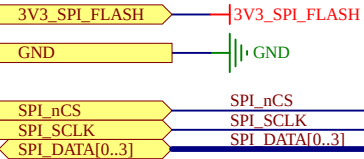
(9-1) SDRAM-32MB



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10.QSPI Flash

(10-1) QSPI Flash

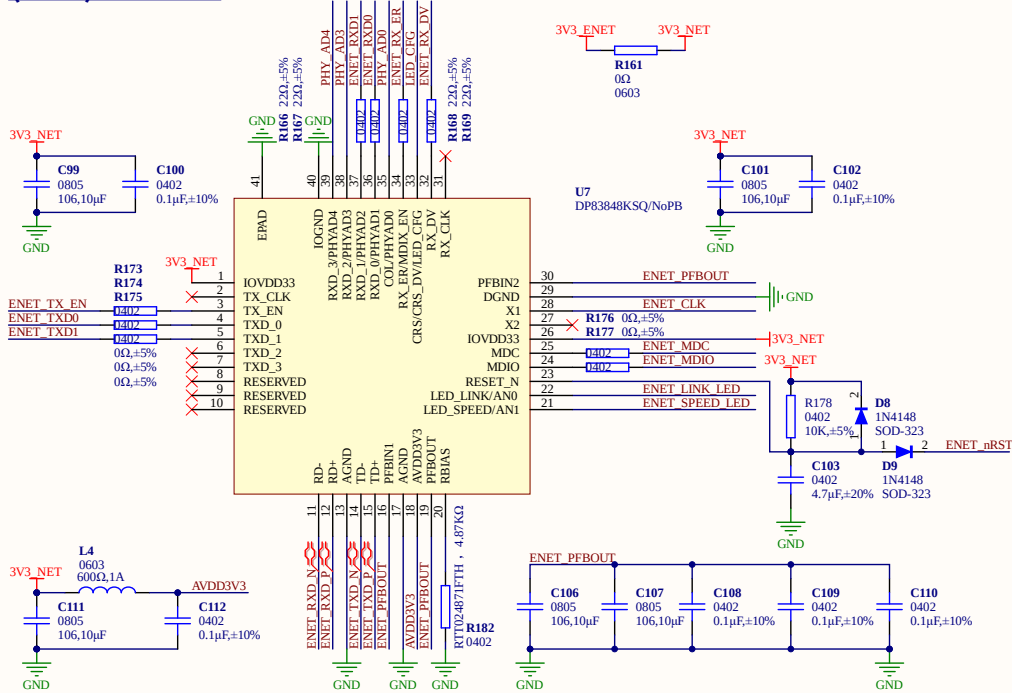


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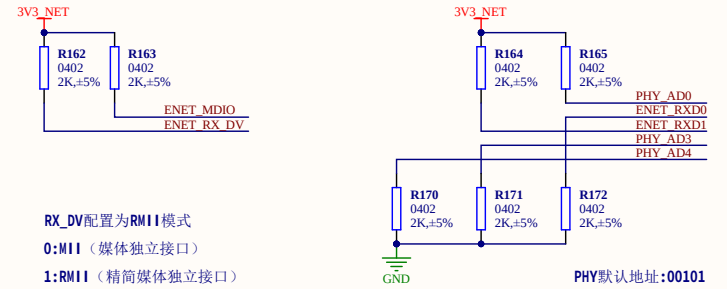
11. ENET_10/100Mbps



(11-1) ENET PHY



(11-2) PHY CFG & ADD

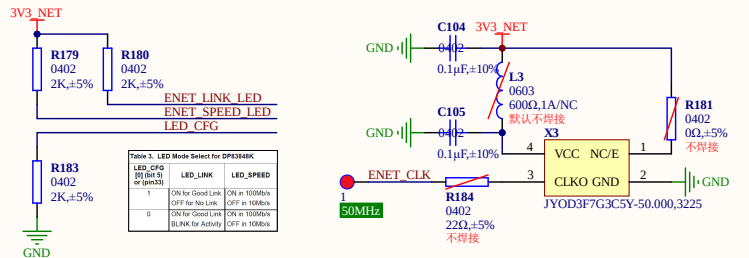


RX_DV配置为RMII模式

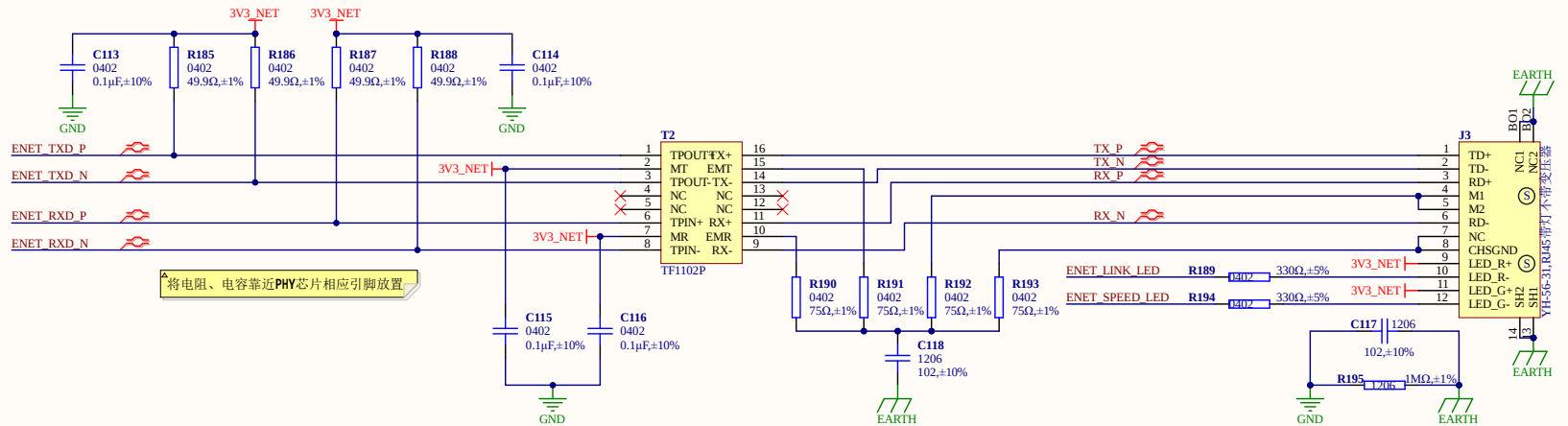
0:MII (媒体独立接口)

1:RMI（精简媒体独立接口）

(11-3) PHY CFG & CLK



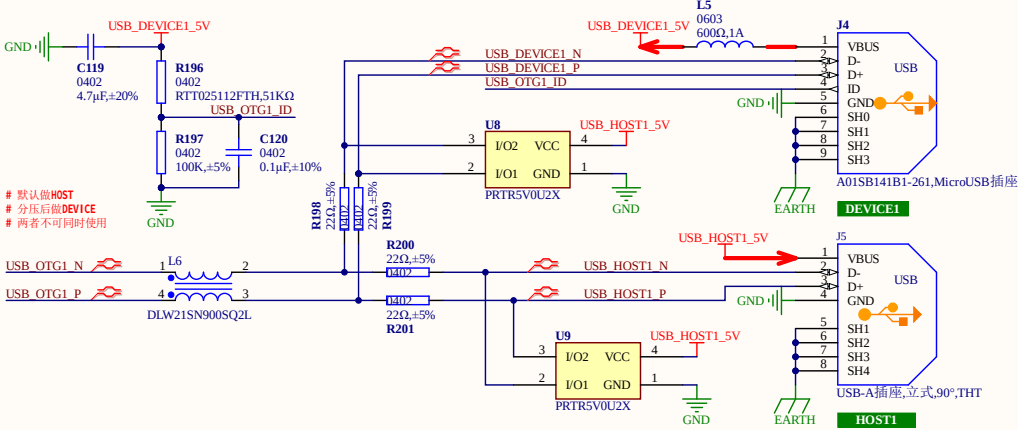
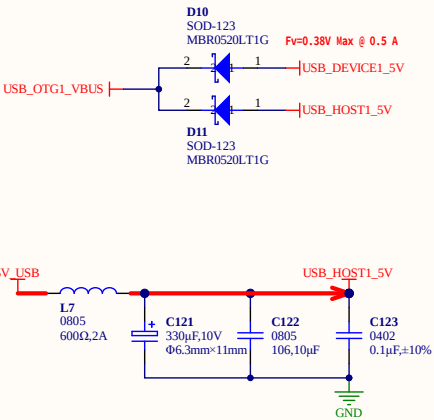
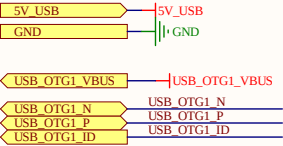
(11-4) PHY Analog



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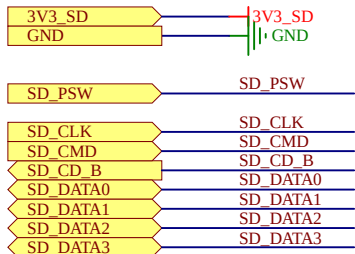
12.USB OTG1

(12-1) USB OTG1

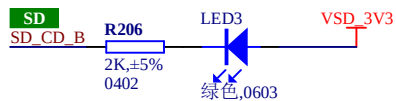


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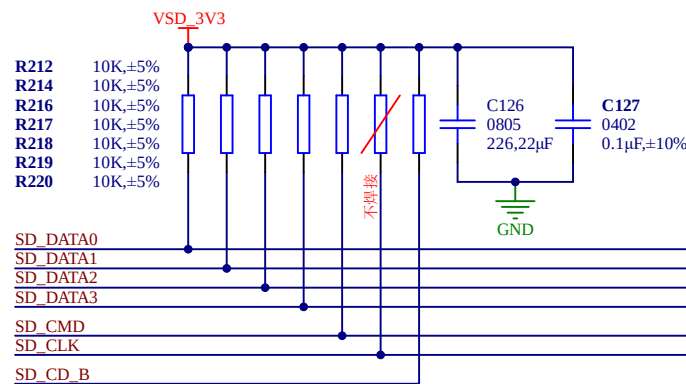
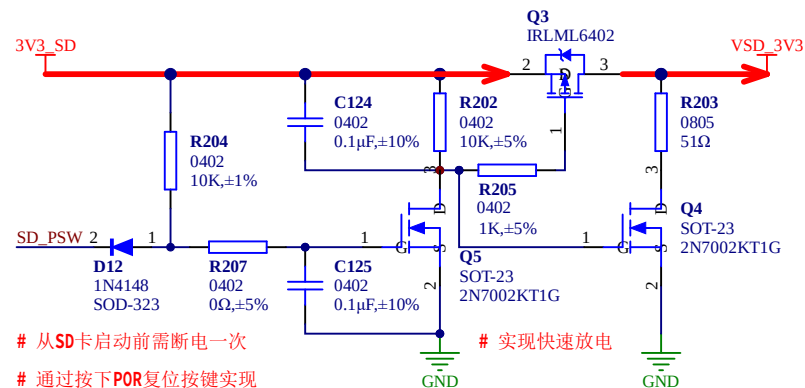
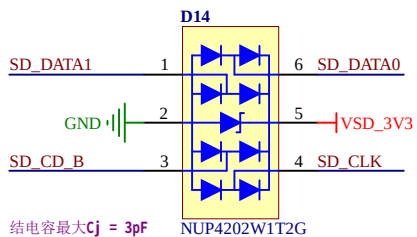
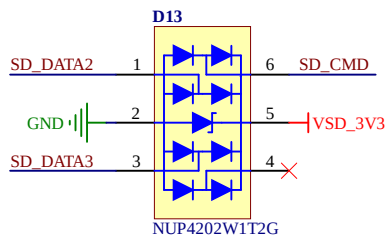
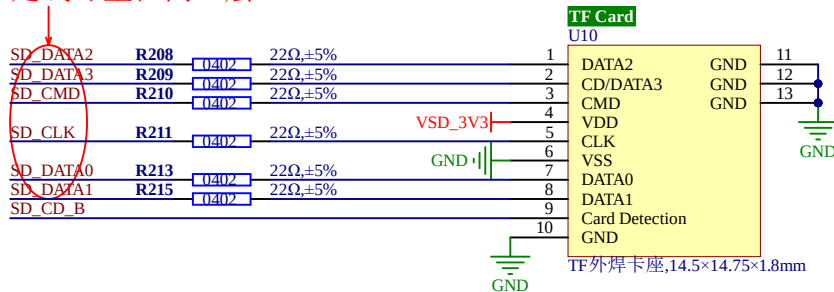
13.SD Card



(13-1) SD CARD(3V3)

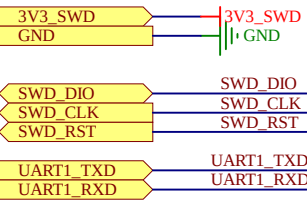


尽量平行等长处理
走线尽量在同一层

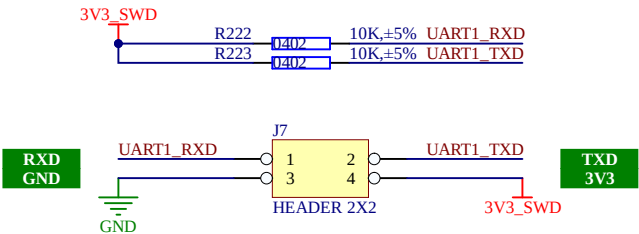
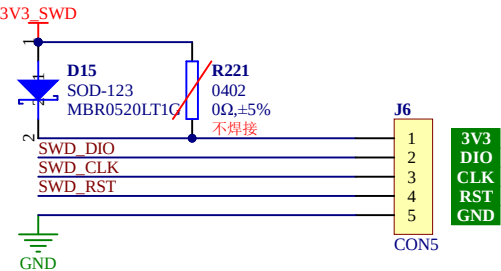


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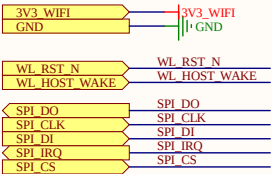
14.SWD & UART1



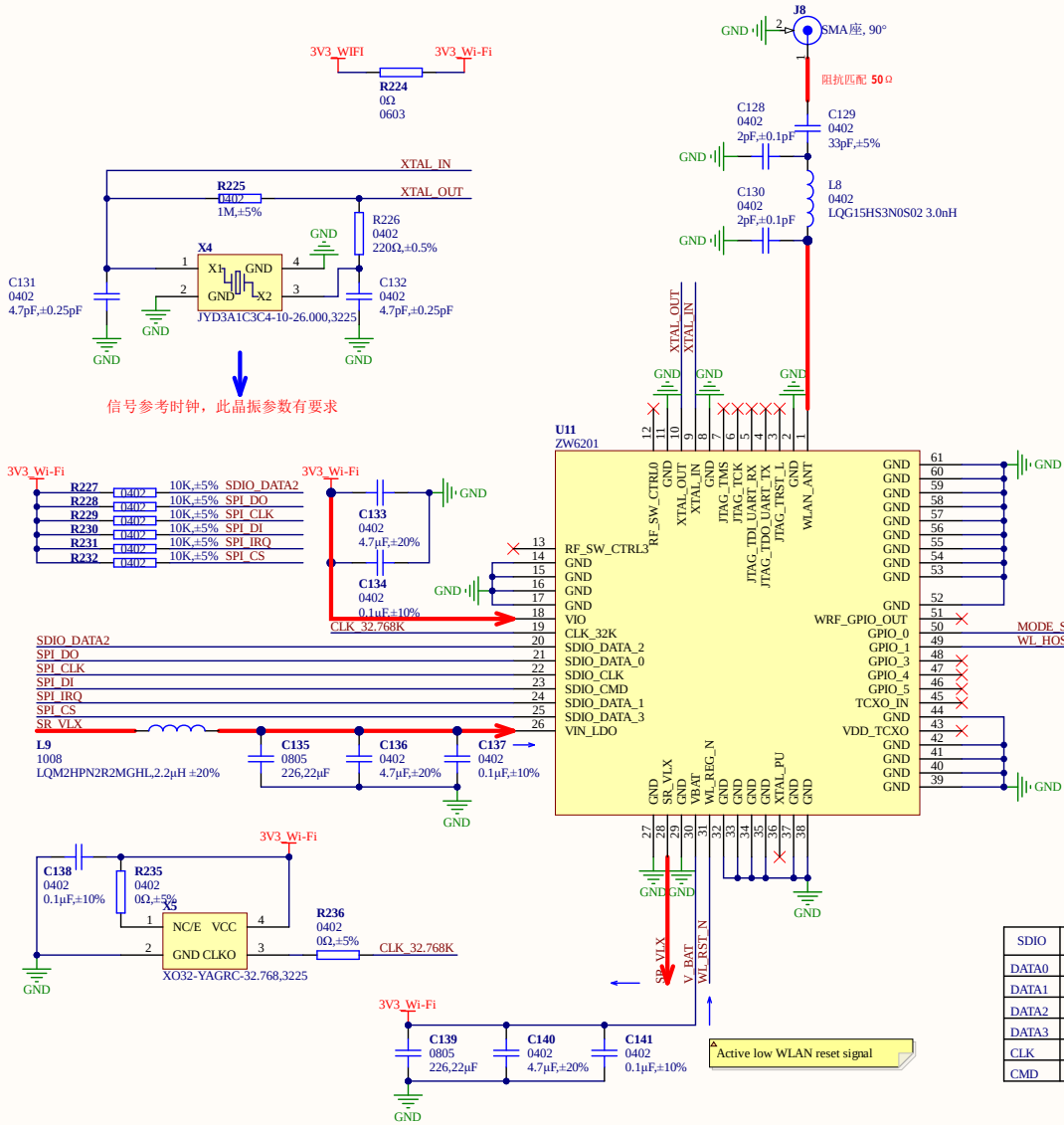
(14-1) SWD & UART1



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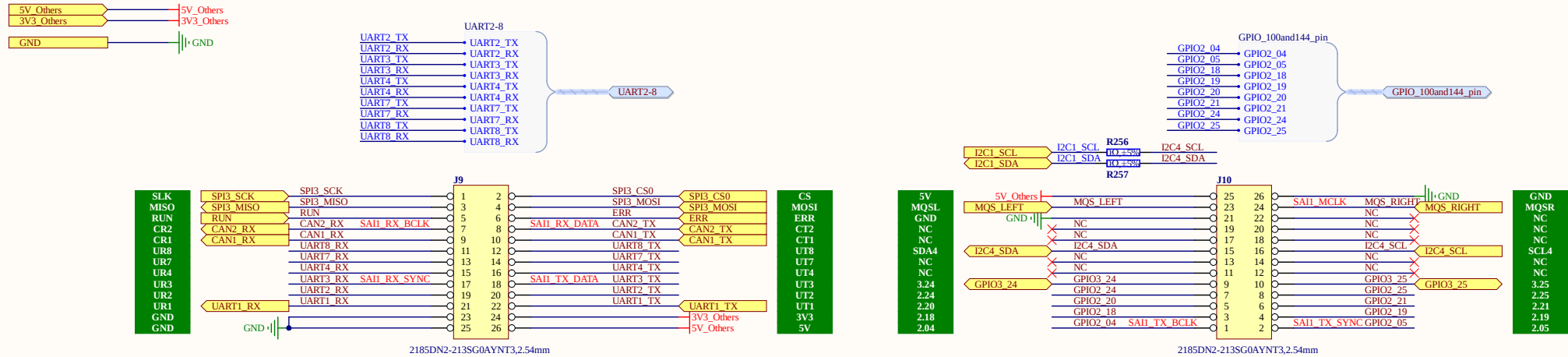


(15-1) Wi-Fi

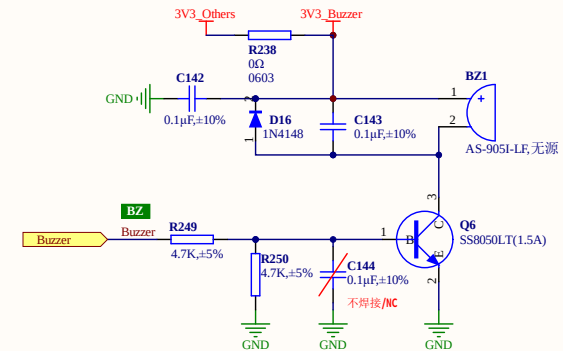
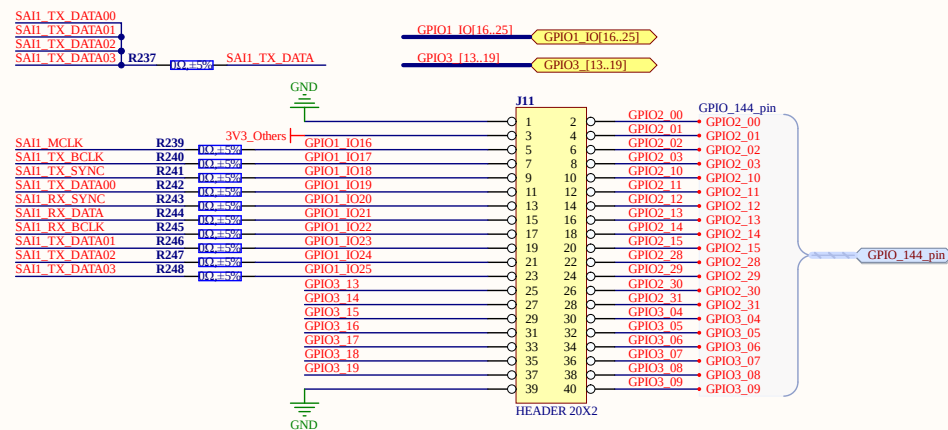


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(16-1) Others-1



(16-2) Others- 2 & Buzzer



#	修改日期	修改内容
1		
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