



# MID3W2424A

## 3W, 24Vin Isolated Power Module for IGBT/SiC Bias Supply

**PRELIMINARY SPECIFICATIONS SUBJECT TO CHANGE**

### DESCRIPTION

The MID3W2424A is an 3W, 24Vin, 3kVrms isolated DC to DC power module with an integrated transformer. This solution is ideal for being used as an isolated power supply for biasing power transistors such as silicon carbide (SiC), silicon FETs, and IGBTs. It supports up to 3W output power with 24V input voltage, and -40°C to 105°C operating ambient temperature. The MID3W2424A simplifies the design of an isolated bias supply by integrating the control, switching power stage, protection circuitry, the transformer, and other passive components in one package. This module provides a small solution size and higher reliability operation compared to a traditional system in package (SIP) isolated power module.

MID3W2424A features different protection features such as over voltage (OVP), over current (OCP), and over temperature protection (OTP). This device uses a soft switching topology for improved EMI performance.

The MID3W2424A requires a minimal number of readily available, standard external components. It is available in a low profile 4.8 mm tall LGA 10x10mm package.

### FEATURES

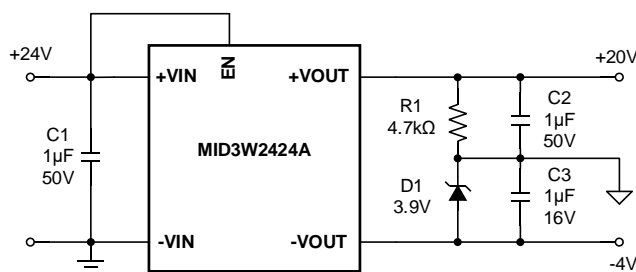
- Optimized for 24V±10% Input Voltage
- 1:1 Input/Output Voltage Ratio
- Up to 3W Output Power
- 500kHz Switching Frequency
- 3kVrms Isolation Test Voltage 'Hi-Pot Test'
- Soft Start, OCP, Input OVP, OTP
- Operating Ambient Temperature: -40°C to 105°C
- EN62368-1 Compliant

### APPLICATIONS

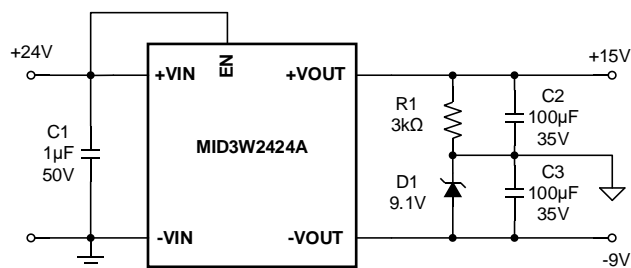
- Isolated Gate Driver

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### TYPICAL APPLICATION



**SIC GATE DRIVER POWER SUPPLY**



**IGBT GATE DRIVER POWER SUPPLY**

**ORDERING INFORMATION**

Part Number*	Package	Top Marking	MSL Rating
MID3W2424AGO-3U	LGA-6 (10mmx10mm)	See Below	3

\* For Tray, add suffix -T (e.g., MID3W2424AGO-3U-T).

**TOP MARKING**

ww: yyxxx \*

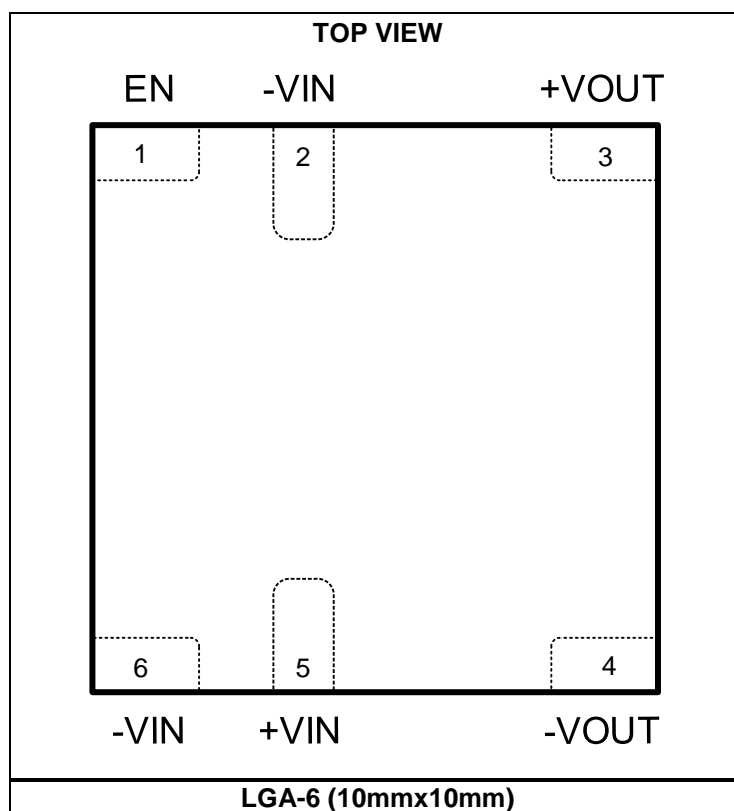
MPS

MIDxWxxxx

\* ww: Week code

yy: Year code

xxx: Lot number

**PACKAGE REFERENCE**



## PIN FUNCTIONS

LGA-6 Pin #	Name	Description
1	EN	<b>Enable.</b> Pull high to enable MID3W2424A, pull low to disable MID3W2424A. EN can be connected to +VIN directly for automatic start-up.
2,6	-VIN	<b>Ground to Input Voltage</b>
3	+VOUT	<b>Output Voltage</b>
4	-VOUT	<b>Ground to Output Voltage</b>
5	+VIN	<b>Input Voltage.</b> Place a 1uF ceramic capacitor close to Pin 5 and Pin 6 for good decoupling.

ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup>

VIN, EN ..... -0.3V to 36V  
 Junction Temperature ..... 150°C  
 Lead Temperature ..... 260°C  
 Storage Temperature ..... -65°C to +150°C  
 Continuous Power Dissipation ( $T_A = +25^\circ\text{C}$ ) <sup>(2)</sup>  
 ..... 1.2W

Recommended Operating Conditions <sup>(3)</sup>

Input Voltage VIN ..... 10.8V to 26.4V  
 Ambient Temp. ( $T_A$ ) ..... -40°C to +105°C

## Thermal Resistance

 $\theta_{BA}$  <sup>(4)</sup>

MID3W2424A ..... 45.6 °C/W  
 EV3W2424A-O-00A <sup>(5)</sup> ..... 32.6 °C/W

## Notes:

- 1) Exceeding these ratings may damage the device.
- 2) The maximum allowable power dissipation is a function of the maximum junction temperature  $T_J$  (MAX), the junction-to-ambient thermal resistance  $\theta_{JA}$ , and the ambient temperature  $T_A$ . The maximum allowable continuous power dissipation at any ambient temperature is calculated by  $P_D$  (MAX) =  $(T_J$  (MAX) -  $T_A$ ) /  $\theta_{JA}$ . Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
- 3) The device is not guaranteed to function outside of its operating conditions.
- 4) MID3W2424A module board-to-ambient thermal resistance, tested with still air.
- 5) Measured on EV3W2424A-O-00A, 2-layer PCB.



## ELECTRICAL CHARACTERISTICS

$V_{IN} = 24V$ ,  $T_J = -40^{\circ}C$  to  $+105^{\circ}C$ , Typical value is tested at  $T_J = +25^{\circ}C$ , unless otherwise noted.

Parameter	Symbol	Condition	Min	Typ	Max	Units
<b>Power Supply Transformer Driver</b>						
Nominal Switching Frequency	$F_S$	$V_{IN}=24V$ , full load range		500		kHz
Input Over Voltage Protection Threshold	$V_{OVP\_T}$	Rising	31	33	35	V
	$V_{OVP\_F}$	Falling	28.5	30	32.5	V
Input OVP Hysteresis	$V_{OVP\_H}$			3		V
EN Turn on Threshold	$V_{EN\_ON}$	$V_{EN}$ rising	1.0	1.1	1.2	V
EN Turn off Threshold	$V_{EN\_OFF}$	$V_{EN}$ falling	0.8	0.9	1.0	V
EN Hysteresis	$V_{EN\_HYS}$			0.2		V
Soft-Start Time <sup>(6)</sup>	$t_{SS}$			20		ms
Thermal Shutdown <sup>(6)</sup>	$T_{SD}$			170		$^{\circ}C$
Thermal Shutdown Hysteresis <sup>(6)</sup>	$\Delta T_{SD}$			25		$^{\circ}C$
<b>System Level Parameters</b>						
Nominal Input Voltage				24		V
Input Current at Rated Load				150		mA
No Load Input Current	$I_Q$	$V_{IN}=24V$ , $V_{EN} = 3.3V$		10		mA
Efficiency		125mA load		87.5		%
Maximum Capacitive Load <sup>(6)</sup>		Capacitive load, module starts-up without entering OCP hiccup		470		$\mu F$
Load Regulation		From 10% load to full load		4	6	%
Line Regulation		Input voltage change $\pm 1\%$		$\pm 1.1$		%/%
No-Load Output Voltage		Tested on Typical Application Circuit		26		V
		Tested on Typical Application Circuit with Zener diode disconnected		31.5		
Voltage Set Point Accuracy		125mA load, $V_{OUT,NOM}=24.5V$	+1.5	+2.6	+3.8	%
OC Threshold			130%			$I_O$
Product Temperature Rise above Ambient <sup>(6)</sup>		125mA load		27		$^{\circ}C$
Operation with Temperature Rise				See derating graph		
Isolation Voltage		Pin 1-2 and 5-6 to Pin 3-4, qualified test with 3kVAC, 1min Production test with 3kVAC, 1s	3			kVrms

**ELECTRICAL CHARACTERISTICS**

$V_{IN} = 24V$ ,  $T_J = -40^{\circ}C$  to  $+105^{\circ}C$ , Typical value is tested at  $T_J = +25^{\circ}C$ , unless otherwise noted.

Parameter	Symbol	Condition	Min	Typ	Max	Units
Isolation Capacitance		Pin 1-2 and 5-6 to Pin 3-4, 1MHz/0.1V		4		pF
Isolation Resistance		Pin 1-2 and 5-6 to Pin 3-4, 500VDC	50			GΩ
CMTI		$V_{cm}=650V$	100			kV/us
Ripple & Noise		Peak to peak value, 125mA load, tested on Typical Application Circuit <sup>(7)</sup>		75		mV
MTTF		MIL 217		64		kyr
<b>Safety Regulation</b>						
ESD		IEC/EN61000-4-2		±4		kV
Electrical Fast Transient (EFT) / Burst Transients		IEC/EN61000-4-4		±2		kV
Surge Immunity		IEC/EN61000-4-5, Input to Output		±2		kV

**Note:**

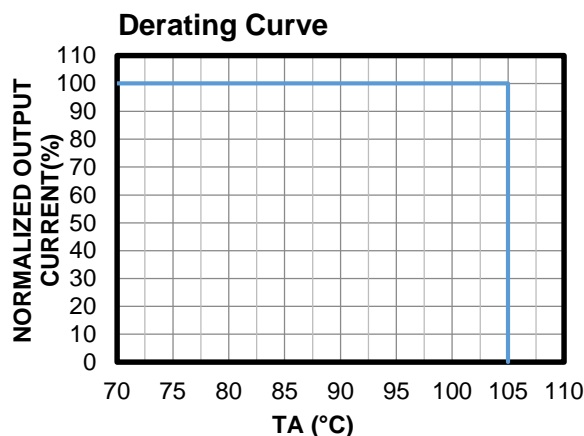
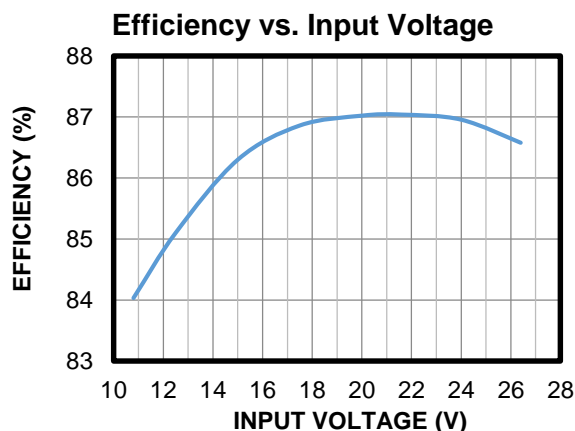
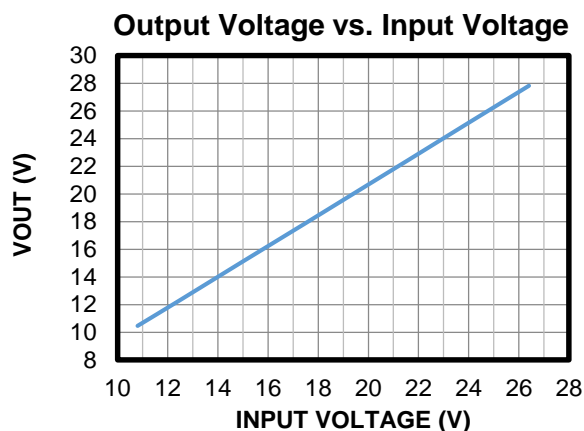
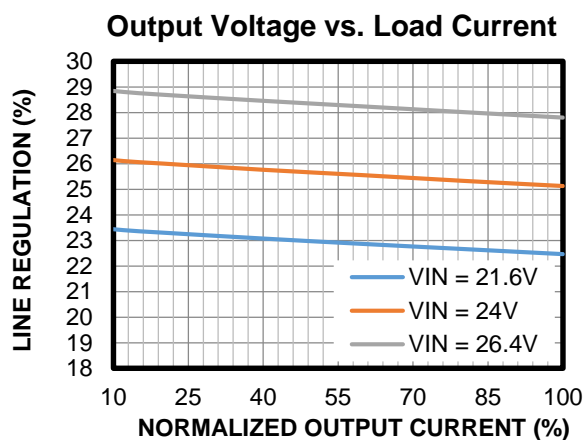
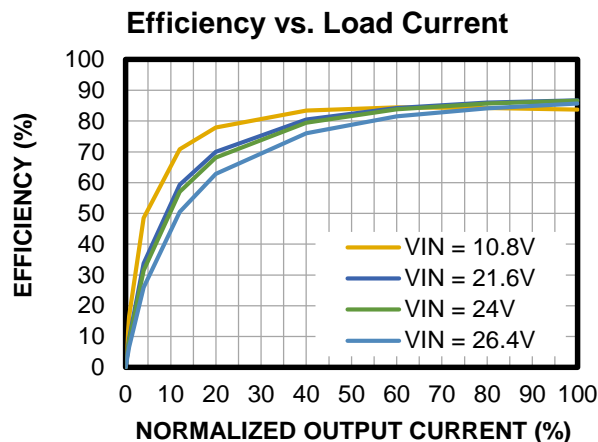
6) Tested by bench characterization only

7) Refer to Figure 2



## TYPICAL PERFORMANCE CHARACTERISTICS

$V_{IN} = 24V$ ,  $I_{out} = 0.125A$ ,  $T_A = -40^{\circ}C$  to  $+105^{\circ}C$ , Typical value is tested at  $T_A = +25^{\circ}C$ , unless otherwise noted.





## TYPICAL PERFORMANCE CHARACTERISTICS (continued)

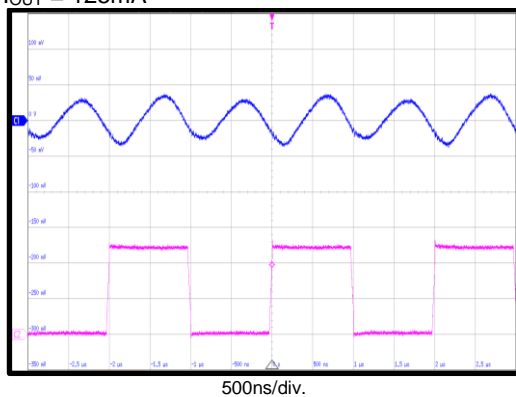
$V_{IN} = 24V$ ,  $I_{OUT} = 0.125A$ ,  $T_A = +25^{\circ}C$ , unless otherwise noted.

## Steady State

$I_{OUT} = 125mA$

CH1:  $V_{OUT}$   
AC  
50mV/div.

CH2:  $V_{SW}$   
10V/div.

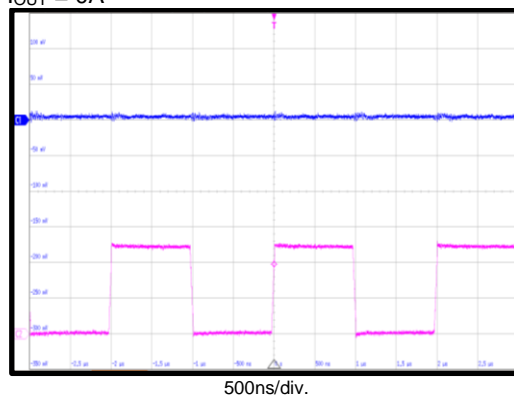


## Steady State

$I_{OUT} = 0A$

CH1:  $V_{OUT}$   
AC  
50mV/div.

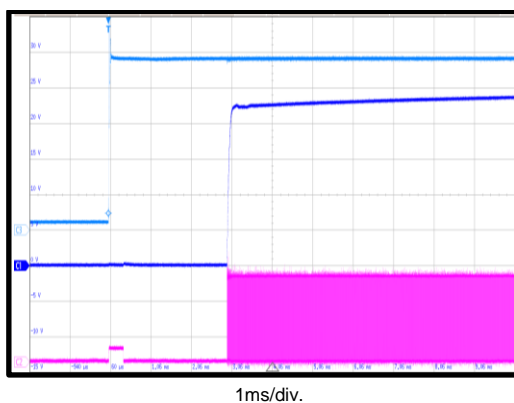
CH2:  $V_{SW}$   
10V/div.

Start-Up through  $V_{IN}$ 

CH3:  $V_{IN}$   
5V/div.

CH1:  $V_{OUT}$   
5V/div.

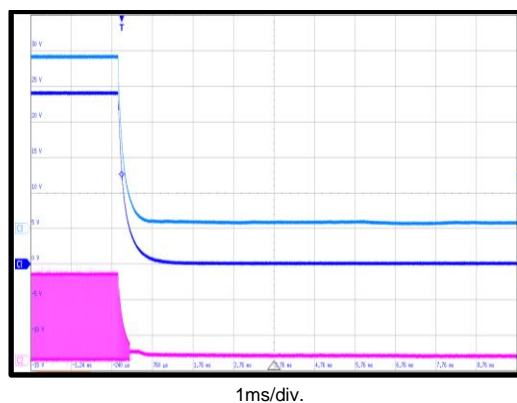
CH2:  $V_{SW}$   
10V/div.

Shutdown through  $V_{IN}$ 

CH3:  $V_{IN}$   
5V/div.

CH1:  $V_{OUT}$   
5V/div.

CH2:  $V_{SW}$   
10V/div.

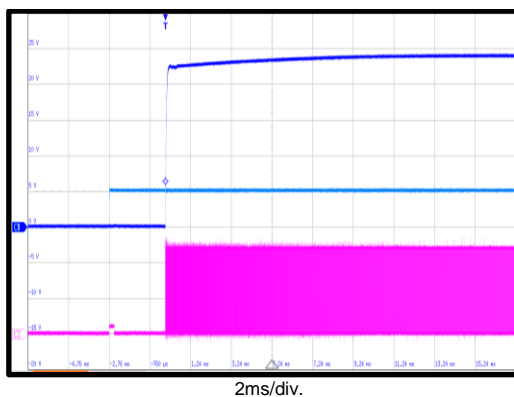


## Start-Up through EN

CH1:  $V_{OUT}$   
5V/div.

CH3: EN  
5V/div.

CH2:  $V_{SW}$   
10V/div.

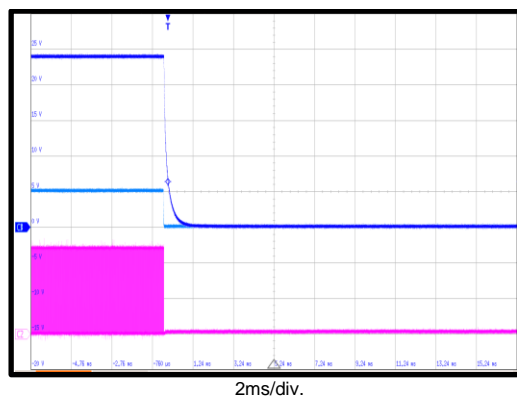


## Shutdown through EN

CH1:  $V_{OUT}$   
5V/div.

CH3: EN  
5V/div.

CH2:  $V_{SW}$   
10V/div.





## TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{IN} = 24V$ ,  $I_{OUT} = 0.125A$ ,  $T_A = +25^{\circ}C$ , unless otherwise noted.

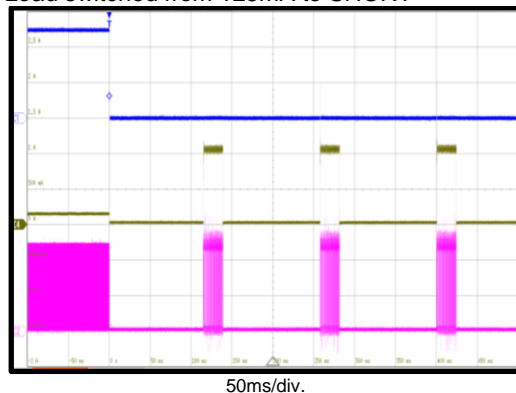
## SCP Entry

Load switched from 125mA to SHORT

CH1:  $V_{OUT}$   
10V/div.

CH4:  $I_{OUT}$   
0.5A/div.

CH2:  $V_{SW}$   
10V/div.



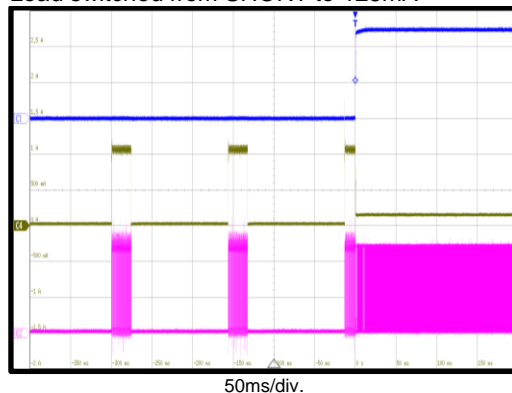
## SCP Recovery

Load switched from SHORT to 125mA

CH1:  $V_{OUT}$   
10V/div.

CH4:  $I_{OUT}$   
0.5A/div.

CH2:  $V_{SW}$   
10V/div

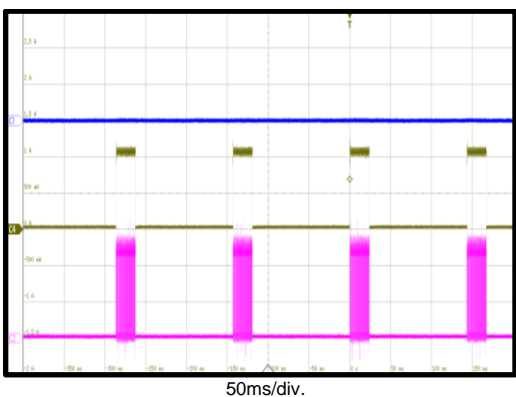


## SCP Steady State

CH1:  $V_{OUT}$   
10V/div.

CH4:  $I_{OUT}$   
0.5A/div.

CH2:  $V_{SW}$   
10V/div.

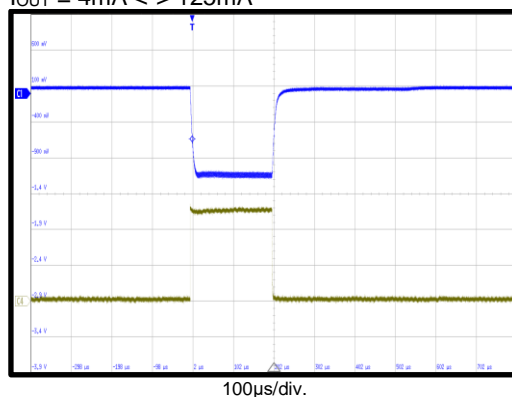


## Load Transient Response

$I_{OUT} = 4mA \leftrightarrow 125mA$

CH1:  $V_{OUT}$   
AC  
0.5V/div.

CH4:  $I_{OUT}$   
50mA/div.







## OPERATION

### Enable Control

The EN pin enables and disables the MID3W2424A power converter. When applying a voltage higher than the EN start up threshold the MID3W2424A is enabled and starts switching operation after a delay. The device is disabled when the EN voltage falls below the turn off threshold. EN pin is compatible with voltage up to 30V, for automatic start-up, connect EN pin to VIN pin directly.

### Soft-Start of Power Converter

The MID3W2424A implements a soft start mechanism to limit the inrush current during start-up.

### Over Current & Short Circuit Protection

In over current (OC) condition, the MID3W2424A will limit the transformer current to a constant value. If the OC condition persists for longer than 20ms, the part shuts down and auto-retries after a waiting time.

During an abrupt dead short, the inrush current may trigger short circuit protection. The part will immediately shut down and auto-retries after a waiting time.

### Over Voltage Protection

When input voltage goes above the over voltage protection threshold, the converter immediately stops switching. After the input voltage falls below the OVP threshold (with hysteresis), the part restarts.

### Over Temperature Protection

MID3W2424A constantly monitors the transformer driver temperature. When the transformer driver temperature goes over the thermal shutdown limit, the part shuts down. After the temperature falls back below thermal limit (with hysteresis), the part restarts.



## APPLICATION INFORMATION

### Start-up/Shut-down Threshold

For automatic start-up and shut-down the input voltage threshold can be set by connecting voltage divider between +VIN and -VIN. Referring to Figure 5, when the external EN control is left open, the turn-on and turn-off thresholds are set by the ratio of R4 to R3:

$$V_{IN\_ON(OFF)} = V_{EN\_ON(OFF)} \left( 1 + \frac{R_4}{R_3} \right)$$

Set resistor values so that the load they present to +Vin is acceptable.

### Filtering EN

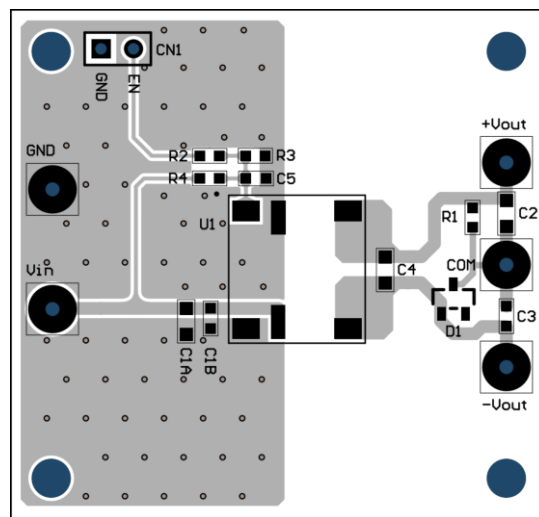
Unless connected directly to +VIN, add 10nF capacitor between EN and -VIN pins to prevent any noise disturbing normal operation.

### PC Board Layout

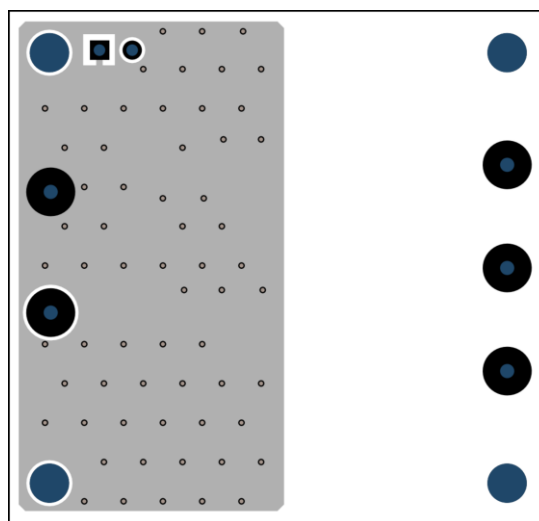
PCB layout is important for normal operation. Refer to Figure 1: Recommended Layout, Figure 5 and the PCB layout guidelines below.

1. To comply with isolation and safety requirements, primary side and secondary side must be physically separated. The creepage and clearance distances must meet the safety standard applicable for the application.
2. In order to leverage CMTI performance of the module, minimize capacitive coupling between primary and secondary side. In this respect, it is best to keep the region defined by the creepage distance, free of any metal objects on any PCB layers.
3. To minimize loop of the input ripple current, use low ESR MLCC type capacitors for local decoupling, placed close to the +VIN and -VIN pins. The smaller value capacitor (C1B) should be placed closer to the pins than the one of larger capacitance (C1A).
4. One must be careful with using large area of copper connected to any net on the isolated side, as it contributes to increased level of common-mode current and radiated emission. Even in high voltage applications, the contribution to the overall system noise can become non-negligible if isolated side copper area gets large with respect to the rest of the system.

5. Adding an LC input filter (not shown) at a distance from the module or put on the BOT side (shielded by the TOP ground plane copper) can help reduce EMI.
6. In applications where high CMTI rating is not a must, EMI performance can be improved by connecting a Y-capacitor across the isolation barrier, which provides low impedance return path for the common-mode noise component. Small value Y-capacitor can also be built by overlapping primary and secondary side copper layers. Care must be taken however, not to violate the required isolation rating.



Top Layer



Bottom Layer

Figure 1: Recommended Layout



## TYPICAL APPLICATION CIRCUITS

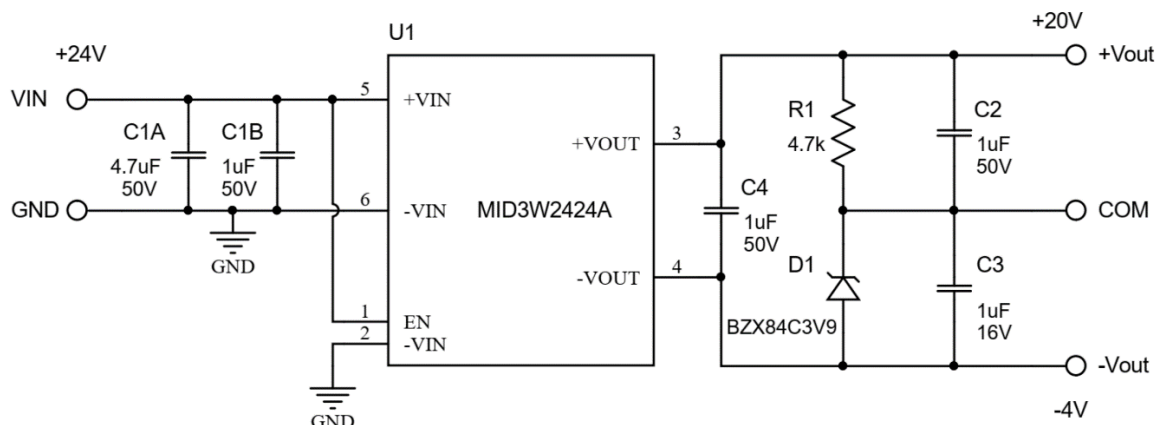


Figure 2: VIN=24V, Vo=+20V/-4V/125mA, automatic start-up

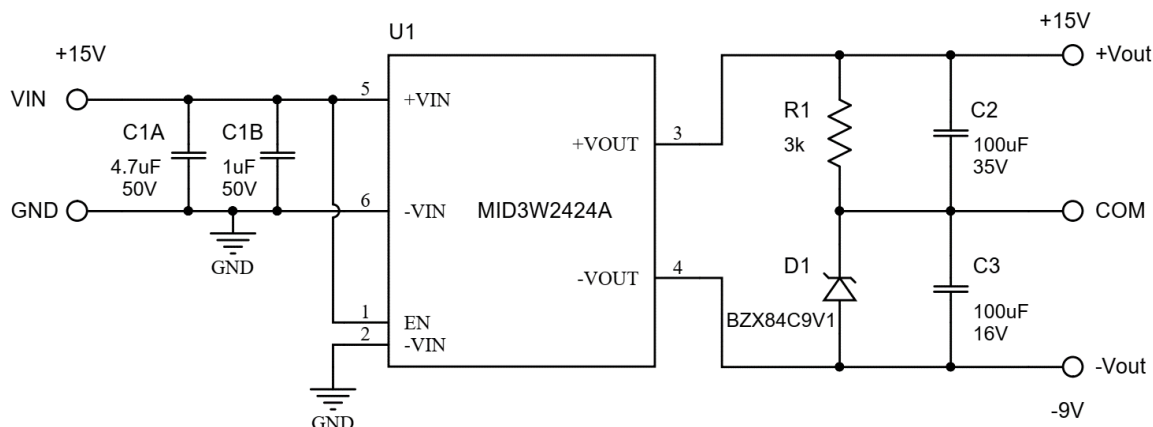


Figure 3: VIN=24V, Vo=+15V/-9V/125mA, automatic start-up

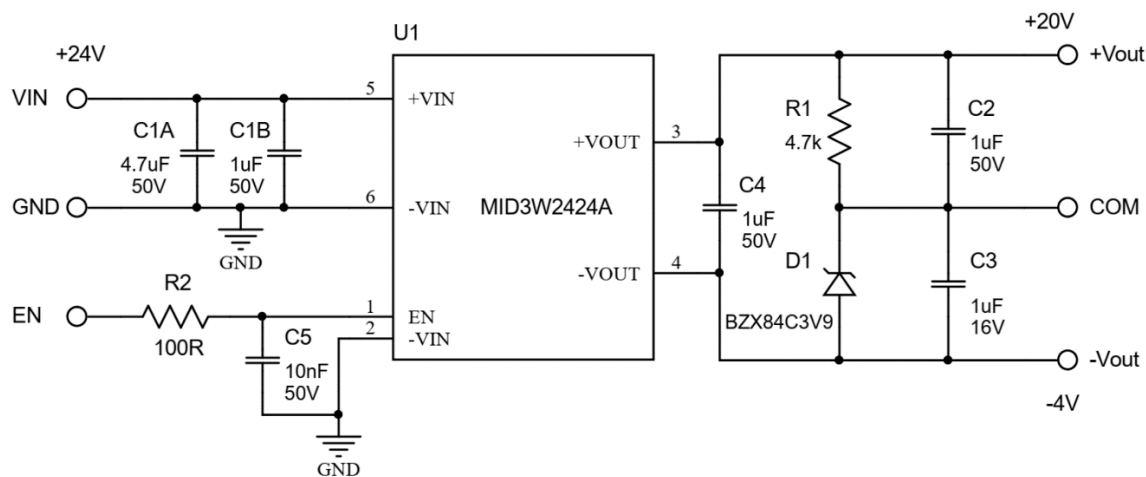
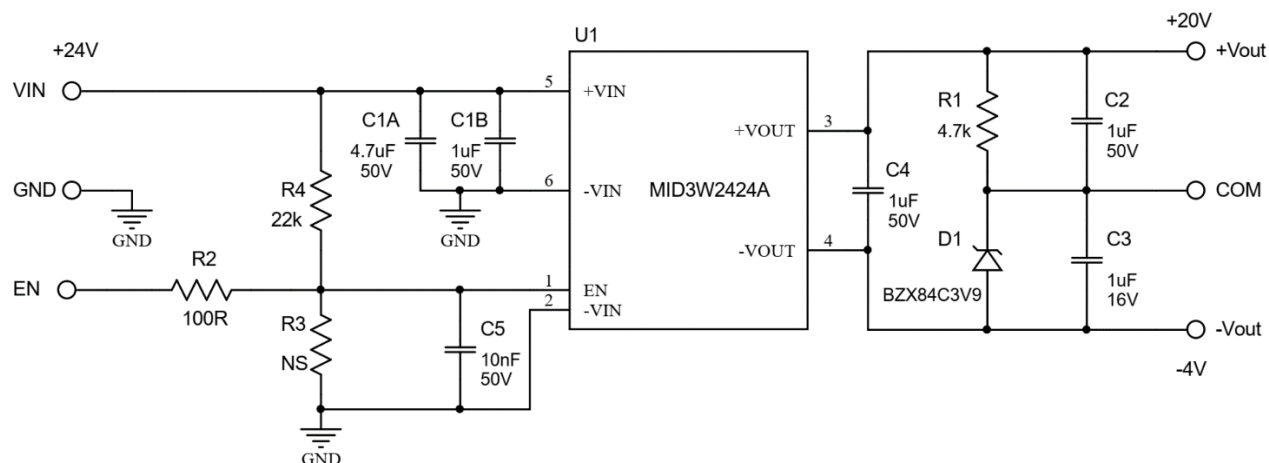


Figure 4: VIN=24V, Vo=+20V/-4V/125mA, pull EN high to start operation



## MID3W2424A – 3W, 24VIN ISOLATED POWER MODULE

**PRELIMINARY SPECIFICATIONS SUBJECT TO CHANGE**

**Figure 5: VIN=24V, Vo=+20V/-4V/125mA; start device either by applying power or controlling EN**

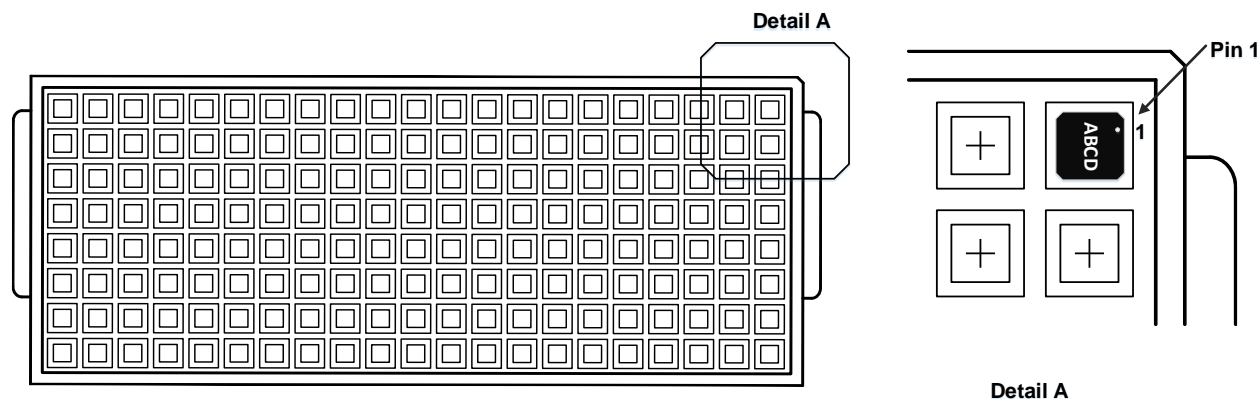
### LGA-6 (10mmx10mmx5mm)



- 1) THE LEAD SIDE IS WETTABLE.
- 2) ALL DIMENSIONS ARE IN INCHES [MILLIMETERS].
- 3) DRAWING IS NOT TO SCALE.



## CARRIER INFORMATION



**Note:**

This is a schematic diagram of Tray. Different packages correspond to different trays with different length, width and height.

Part Number	Package Description	Quantity/ Reel	Quantity/ Tube	Quantity/ Tray	Reel Diameter	Carrier Tape Width	Carrier Tape Pitch
MID3W2424AGO-3U-T	LGA-6 (10mmx10mm)	N/A	N/A	152	N/A	N/A	N/A

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